

PRODUCT SPECIFICATIONS

For Customer: _____

☐ : APPROVAL FOR SPECIFICATION

Customer Model No. _____

☐ : APPROVAL FOR SAMPLE

Module No.: BD-T101BAH-02

Date : 2015-01-07

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For Customer's Acceptance:

Approved By	Comment

PREPARED	CHECKED	VERIFIED BY QA DEPT	VERIFIED BY R&D DEPT

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2. Revision Record

Date	Rev.No.	Page	Revision Items	Prepared
2015-01-07	V0		The first release	ZHP

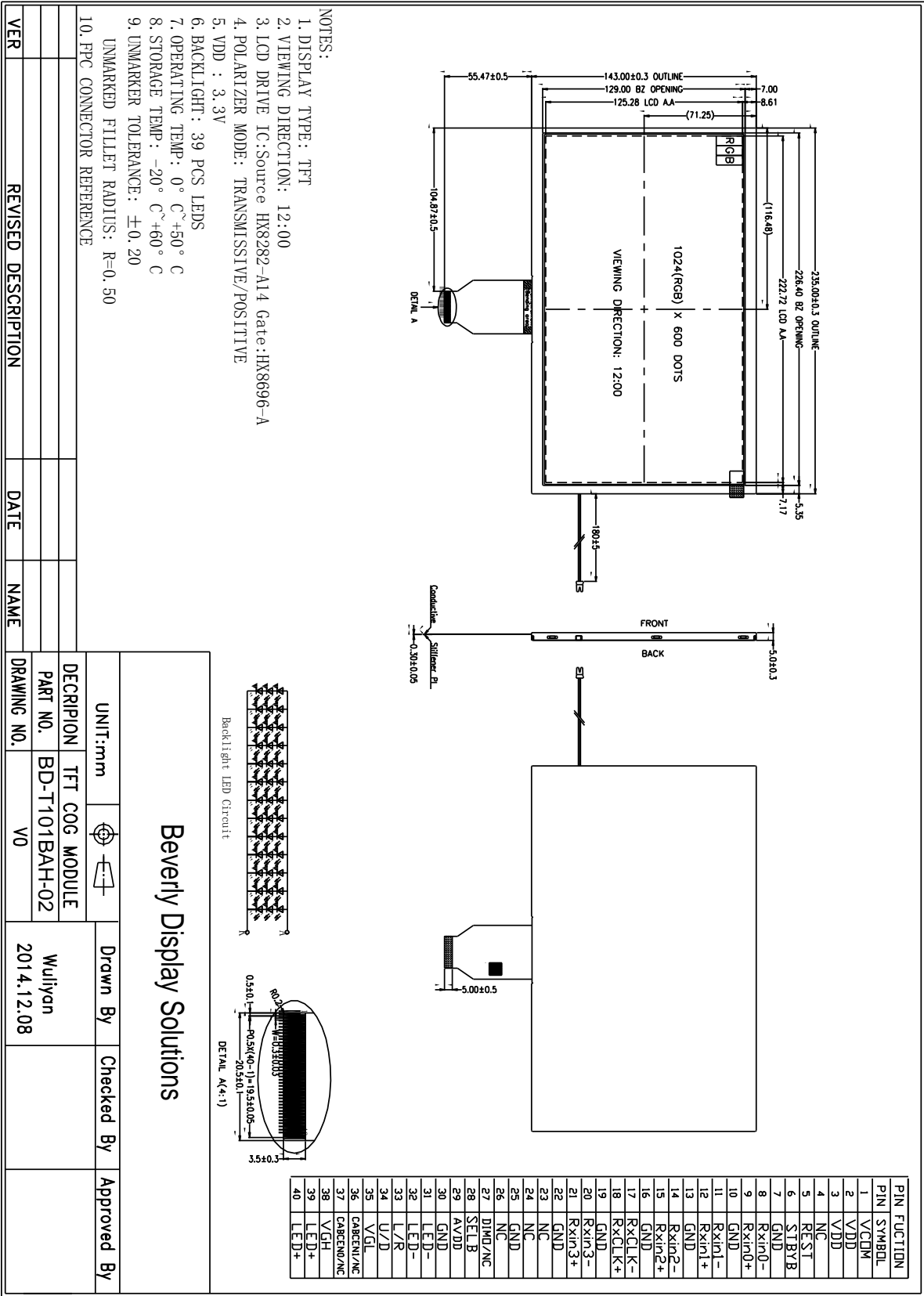
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3. General Specifications

BD-T101BAH-02 is a TFT-LCD module. It is composed of a TFT-LCD panel, driver IC, FPC, a back light unit. The 10.1" display area contains 1024 x 600 pixels and can display up to 16.7M colors. This product accords with RoHS environmental criterion.

Item	Contents	Unit	Note
LCD Type	TFT	-	
Display color	16.7M		
Viewing Direction	12	O'Clock	
Gray scale inversion direction	6	O'Clock	
Operating temperature	-20~+70	℃	
Storage temperature	-30~+80	℃	
Module size	Refer to outline drawing	mm	
Active Area(W×H)	222.27X125.28	mm	
Number of Dots	1024×600	dots	
Controller	HX8282A14+HX8696	-	
Power Supply Voltage	3.3	V	
Outline Dimensions	Refer to outline drawing	-	
Backlight	3X13-LEDs (white)	pcs	
Weight	---	g	
Interface	LVDS	-	

4. Outline Drawing



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5. Absolute Maximum Ratings(Ta=25°C)

5.1 Electrical Absolute Maximum Ratings.(Vss=0V ,Ta=25°C)

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V _{DD}	-0.3	5.0	V	1, 2
	AV _{DD}	6.5	13.5	V	
	V _{GH}	-0.3	42.0	V	
	V _{GL}	-20	0.3	V	
	V _{GH} -V _{GL}	-	40.0	V	

Notes:

1. If the module is above these absolute maximum ratings. It may become permanently damaged.

Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.

2. V_{CC} > V_{SS} must be maintained.

5.2 Typical operation conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	VDD	3.0	3.3	3.6	V	
	AVDD	10.2	10.5	10.8	V	
	VGH	20	21	22	V	
	VGL	-5.0	-5.5	-6.0	V	
Input signal voltage	VCOM	3.3	3.7	4.2	V	
Input logic high voltage	VIH	0.7 VDD	-	VDD	V	
Input logic low voltage	VIL	0	-	0.3VDD	V	

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5.3 Environmental Absolute Maximum Ratings.

Item	Storage		Operating		Note
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-30°C	80°C	-20°C	70°C	1,2
Humidity	-	-	-	-	3

1. The response time will become lower when operated at low temperature.
2. Background color changes slightly depending on ambient temperature.

The phenomenon is reversible.

3. Ta≤40°C:85%RH MAX.

Ta>=40°C:Absolute humidity must be lower than the humidity of 85%RH at 40°C.

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6. Electrical Specifications and Instruction Code

6.1 Electrical characteristics(V_{ss}=0V ,T_a=25°C)

Parameter		Symbol	Condition	Min	Typ	Max	Unit	Note
Power supply		V _{CC}	T _a =25°C	3.0	3.3	3.6	V	
Input voltage	‘H’	V _{IH}	V _{CC} =3.3V	0.8V _{CC}	-	V _{CC}	V	
	‘L’	V _{IL}	V _{CC} =3.3V	0	-	0.2V _{CC}	V	
Current Consumption		I _{CC1}	Normal mode	-	50	-	mA	2
		I _{CC2}	Sleep mode	-	0.2	-	mA	2
Clock Frequency		f _{CLK}	-	-	50	-	MHz	

Note:

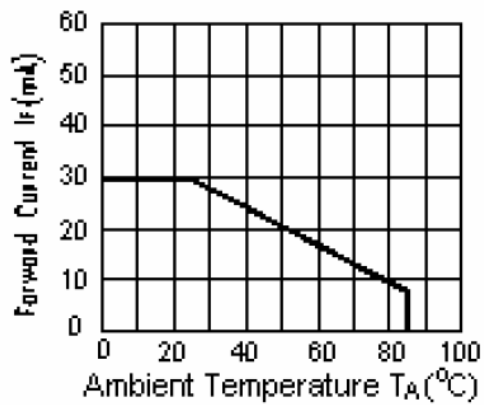
1:When an optimum contrast is obtained in transmissive mode.

2: Tested in 1×1 chessboard pattern.

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6.2 LED backlight specification(VSS=0V ,Ta=25°C)

Item	Symb ol	Condition	Min	Typ	Max	Unit	Note
Supply voltage	Vf	If=20X13mA	-	9.0	-	V	
Uniformity	Δ Bp	If=20X13mA	70			%	
Luminance for LCD	Lv	If=20X13mA	-	350		Cd/m2	
Life Time	T	If=20X13mA	-	30000		Hours	



ILED VS TEMP

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6.3 Interface signals

Pin No.	Symbol	I/O	Function
1	VCOM	P	Common voltage
2-3	VDD	P	Power for digital circuit
4	NC	-	No connect
5	REST	I	Global reset pin
6	SBYB	I	Stand mode: SBYB=1,normal operation SBYB=0,timing control,source driver will turn off,all output are hight-Z
7	GND	P	Ground
8	RXIN0-	I	-LVDS differential data input
9	RXIN0+	I	+LVDS differential data input
10	GND	P	Ground
11	RXIN1-	I	-LVDS differential data input
12	RXIN1+	I	+LVDS differential data input
13	GND	P	Ground
14	RXIN2-	I	-LVDS differential data input
15	RXIN2+	I	+LVDS differential data input
16	GND	P	Ground
17	RXCLKIN-	I	-LVDS differential clock input
18	RXCLKIN+	I	+LVDS differential clock input
19	GND	P	Ground
20	RXIN3-	I	-LVDS differential data input
21	RXIN3+	I	+LVDS differential data input
22	GND	P	Ground
23-24	NC	-	No connection
25	GND	P	Ground
26	NC	-	No connection
27	DIMO	I	Backlight CABAC controller signal output
28	SELB	I	6bit/8bit select H:6bit, L:8bit
29	AVDD	P	Power for analog circuit
30	GND	P	Ground
31-32	LED-	P	LED Cathode
33	L/R	I	Horizonal inversion
34	U/D	I	Vertical inversion
35	VGL	P	Negative power for TFT
36	CABCEN1	I	CABC H/W enable

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37	CABCEN0	I	CABC H/W enable
38	VGH	P	Positive power for TFT
39-40	LED+	P	LED Anode

NOTE:

Setting of scan control input		Scanning direction
U/D	L/R	
DV _{DD}	DV _{DD}	Up to down, left to right
GND	DV _{DD}	Down to up, left to right
DV _{DD}	GND	Up to down, right to left
GND	GND	Down to up, right to left

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7. Optical Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Brightness	Bp	$\theta=0^\circ$	-	350	-	Cd/m ²	1
Uniformity	Δ Bp	$\Phi=0^\circ$	70	-	-	%	1,2
Viewing Angle	3:00	$Cr \geq 10$	-	60	-	Deg	3
	6:00		-	45	-		
	9:00		-	60	-		
	12:00		-	60	-		
Contrast Ratio	Cr	$\theta=0^\circ$ $\Phi=0^\circ$	300	500		-	4
Response Time	T _r		-	10	-	ms	5
	T _f		-	10	-	ms	
Color of CIE Coordinate	W	x		0.28		-	1,6
		y		0.33		-	
	R	x		0.51		-	
		y		0.34		-	
	G	x		0.31		-	
		y		0.56		-	
	B	x		0.15		-	
		y		0.14		-	
NTSC Ratio	S		50	60	-	%	

Note: The parameter is slightly changed by temperature, driving voltage and material

Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white.

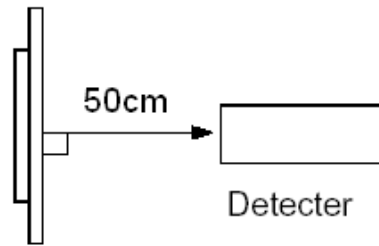
The brightness is the average value of 9 measured spots. Measurement equipment PR-705 (Φ8mm)

Measuring condition:

- Measuring surroundings: Dark room.
- Measuring temperature: Ta=25°C.
- Adjust operating voltage to get optimum contrast at the center of the display.

Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.

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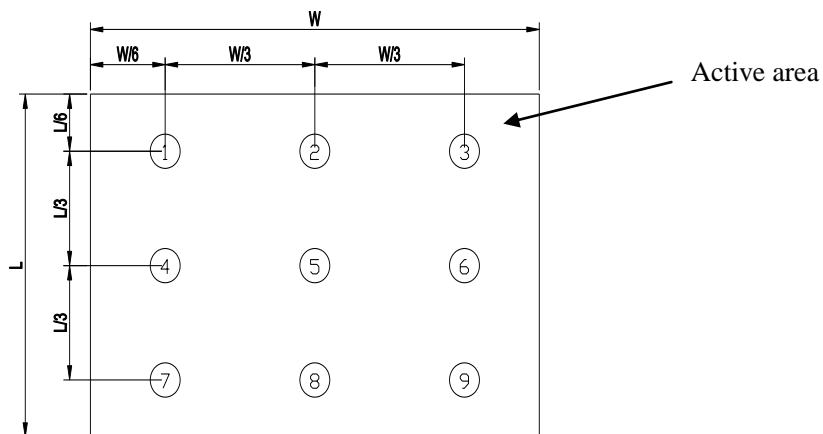


Note 2: The luminance uniformity is calculated by using following formula.

$$\Delta Bp = Bp \text{ (Min.)} / Bp \text{ (Max.)} \times 100 \text{ (\%)}$$

Bp (Max.) = Maximum brightness in 9 measured spots

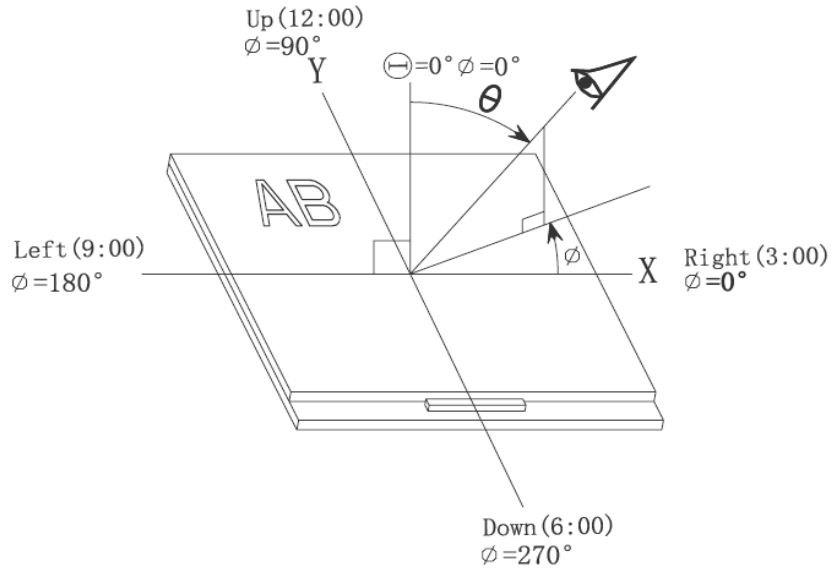
Bp (Min.) = Minimum brightness in 9 measured spots.



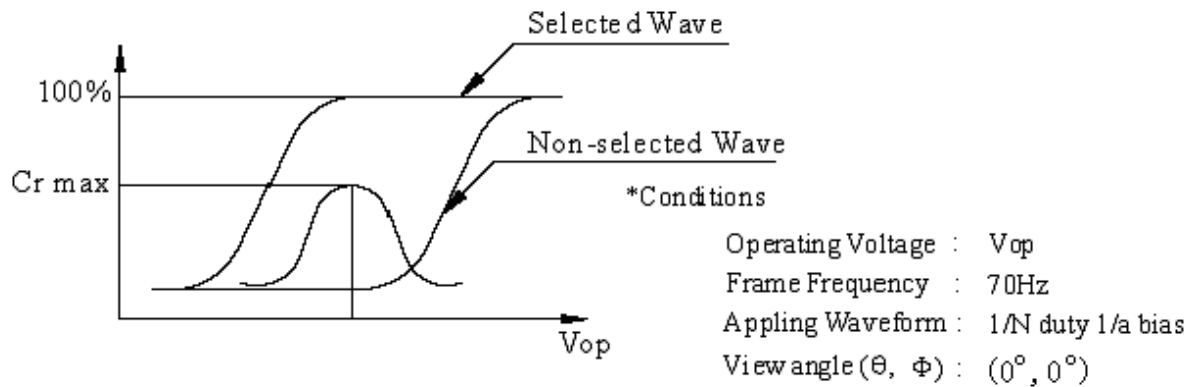
Note 3: The definition of viewing angle:

Refer to the graph below marked by θ and ϕ

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Note 4: Definition of contrast ratio.(Test LCD using DMS501)

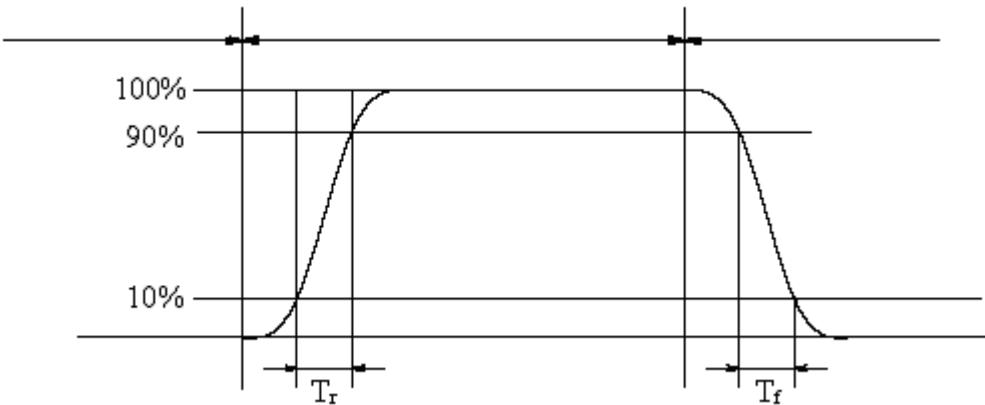


$$\text{Contrast ratio}(Cr) = \frac{\text{Brightness of selected dots}}{\text{Brightness of non-selected dots}}$$

Note 5: Definition of Response time. (Test LCD using DMS501):

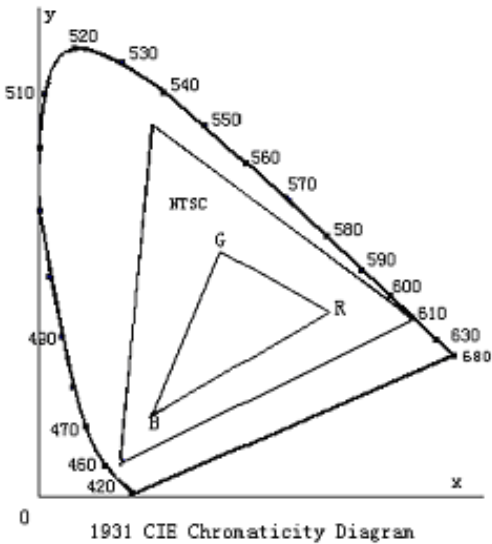
The output signals of photo detector are measured when the input signals are changed from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes.Refer to figure as below.

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The definition of response time

Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.

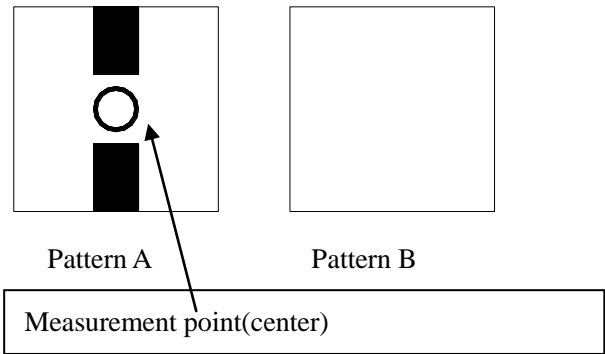


Color gamut:

$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

Note 7: Definition of cross talk.

$$\text{Cross talk ratio(\%)} = \frac{|\text{pattern A Brightness} - \text{pattern B Brightness}|}{\text{pattern A Brightness}} \times 100$$



Electric volume value=3F+/-3Hex

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8. Reliability Test Items and Criteria

No	Test Item	Test condition	Criterion
1	High Temperature Storage	80°C±2°C 96H Restore 2H at 25°C Power off	1. After testing, cosmetic and electrical defects should not happen. 2. Total current consumption should not be more than twice of initial value.
2	Low Temperature Storage	-30°C±2°C 96H Restore 2H at 25°C Power off	
3	High Temperature Operation	70°C±2°C 96H Restore 2H at 25°C Power on	
4	Low Temperature Operation	-20°C±2°C 96H Restore 4H at 25°C Power on	
5	High Temperature/Humidity Operation	60°C±2°C 90%RH 96H Power on	
6	Temperature Cycle	-30°C —————> 80°C 30min 5min 30min after 5 cycle, Restore 2H at 25°C Power off	
7	Vibration Test	10Hz~150Hz, 100m/s ² , 120min	Not allowed cosmetic and electrical defects.
8	Shock Test	Half- sine wave, 300m/s ² , 11ms	

Note: Operation: Supply 2.8V for logic system.

The inspection terms after reliability test, as below

ITEM	Inspection
Contrast	CR>50%
IDD	IDD<200%
Brightness	Brightness>60%
Color Tone	Color Tone+/-0,05

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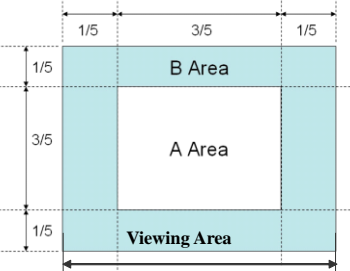
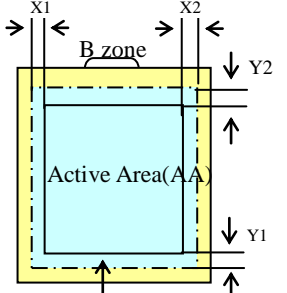
9 Quality level

9.1 Classification of defects

Major defects (MA): A major defect refers to a defect that may substantially degrade usability for product applications, including all functional defects (such as no display, abnormal display, open or missing segment, short circuit, missing component), outline dimension beyond the drawing, progressive defects and those affecting reliability.

Minor defects (MI): A minor defect refers to a defect which is not considered to be able to substantially degrade the product application or a defect that deviates from existing standards almost unrelated to the effective use of the product or its operation, such as black spot, white spot, bright spot, pinhole, black line, white line, contrast variation, glass defect, polarizer defect, etc.

9.2 Definition of inspection range

<p>For dot defect of TFT LCD which is not smaller than 3 inches, dividing three areas to make a judgment (according to figure 1).</p> <p>A area : center of viewing area B area : periphery of viewing area C area : Outside viewing area</p> <p>For other defects, dividing two areas to make a judgment (according figure 2).</p> <p>A zone : Inside Viewing area B zone : Outside Viewing area</p> <p>X1(A.A~V.A): 2mm X2(A.A~V.A): 2mm Y1(A.A~V.A): 2mm Y2(A.A~V.A): 2mm</p>	 <p>Figure 1</p>  <p>Figure 2</p>
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9.3 Inspection items and general notes

General notes	<p>Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and our company.</p> <p>Viewing area should be the area which our company guarantees.</p> <p>Limit sample should be prior to this Inspection standard.</p> <p>Viewing judgment should be under static pattern.</p> <p>Inspection conditions</p> <p>Inspection distance: 250 mm (from the sample) Temperature : 25±5 °C</p> <p>Inspection angle : 45 degrees in 6 o'clock direction (all defects in viewing area should be inspected from this direction)</p>	
Inspection items	Pinhole, Bright spot, Black spot, White spot, Black line, White Line, Foreign particle, Bubble	The color of a small area is different from the remainder. The phenomenon doesn't change with voltage
	Contrast variation	The color of a small area is different from the remainder. The phenomenon changes with voltage
	Polarizer defect	Scratch, Dirt, Particle, Bubble on polarizer or between polarizer and glass

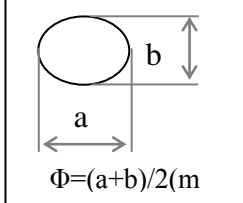
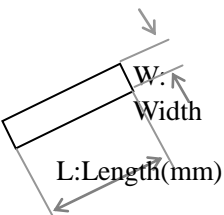
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	Dot defect (TFT LCD)	The pixel appears bright or dark abnormally when display
	Functional defect	No display, Abnormal display, Open or missing segment, Short circuit, False viewing direction
	Glass defect	Glass crack, Shaved corner of glass, Surplus glass
	PCB defect	Components assembly defect

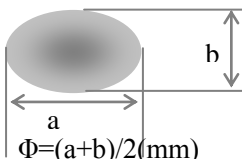
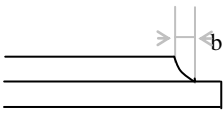
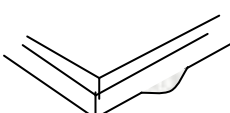
9.4 Outgoing Inspection level

Outgoing Inspection standard	Inspection conditions	Inspection				
		Min.	Max.	Unit	IL	AQL
Major Defects	See 9.3 general notes	See 9.5			II	0.65
Minor Defects	See 9.3 general notes	See 9.5			II	0.65
Note: Sampling standard conforms to GB2828						

9.5 Inspection Items and Criteria

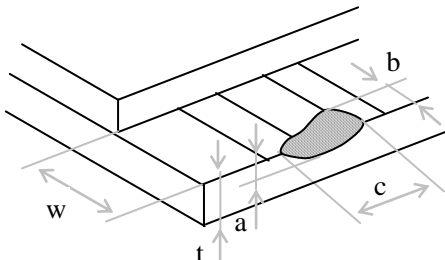
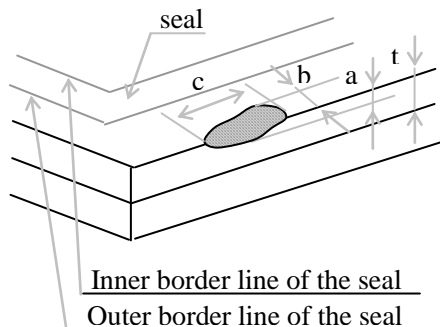
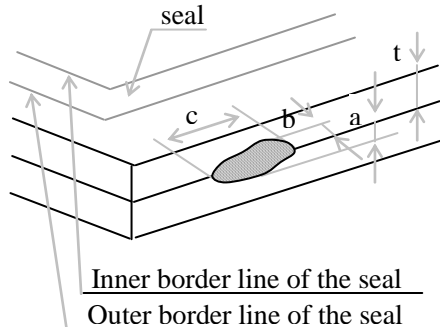
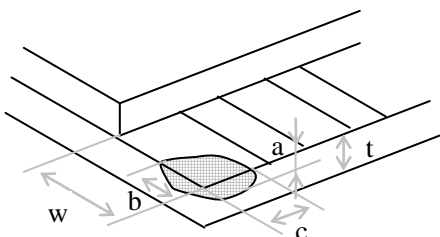
Inspection items			Judgment standard			
			Category		Acceptable number	
					A zone	B zone
1	Black spot, White spot, Pinhole, Foreign Particle, Particle in or on glass, Scratch on glass		A	$\Phi \leq 0.10$	Neglected	Neglected
			B	$0.10 < \Phi \leq 0.2$	1	
			C	$0.2 < \Phi$	0	
			D	-	-	
			Total defective point(B,C)		1	
2	Black line, White line, and Particle Between Polarizer and glass, Scratch on glass		A	$W \leq 0.02$	Neglected	Neglected
			B	$0.02 < W \leq 0.03$ $L \leq 1.0$	1	
			C	$0.03 < W \leq 0.05$ $L > 1.0$	0	
			D	$0.05 < W, 1.0 < L$	0	
			Total defective point(B,C)		1	
3	Bright spot		any size		none	none

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4	Contrast variation		A	$\Phi < 0.2$	Neglected	Neglected
			B	$0.2 < \Phi \leq 0.3$	2	
			C	$0.3 < \Phi \leq 0.4$	1	
			D	$0.4 < \Phi$	0	
			Total defective point(B,C)			3
5	Bubble inside cell		any size		none	none
6	Polarizer defect (if Polarizer is used)	Scratch ,damage on polarizer, Particle on polarizer or between polarizer and glass.	Refer to item 1 and item 2.			
		Bubble, dent and convex	A	$\Phi \leq 0.1$	Neglected	Neglected
			B	$0.1 < \Phi \leq 0.2$	1	
			C	$0.2 < \Phi$	0	
7	Surplus glass	Stage surplus glass 	$B \leq 0.3\text{mm}$			
		Surrounding surplus glass 	Should not influence outline dimension and assembling.			
8	Open segment or open common		Not permitted			
9	Short circuit		Not permitted			
10	False viewing direction		Not permitted			
11	Contrast ratio uneven		According to the limit specimen			
12	Crosstalk		According to the limit specimen			
13	Black /White spot(display)		Refer to item 1			
14	Black /White line(display)		Refer to item 2			

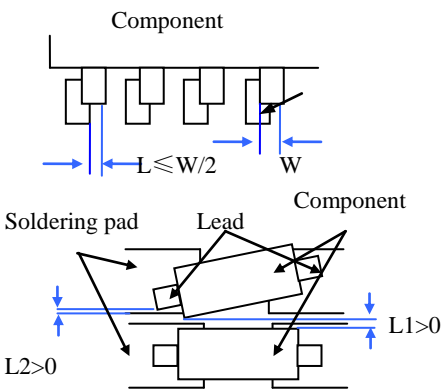
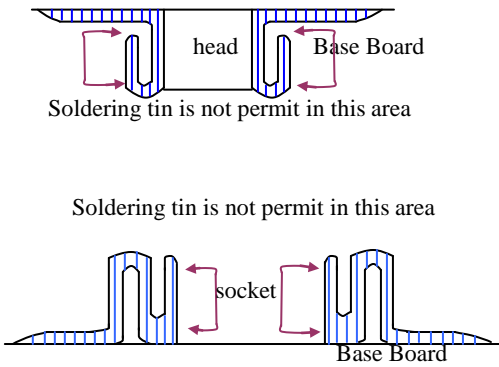
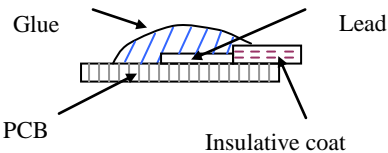
Inspection items	Judgment standard
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			Category(application: B zone)		Acceptable number	
15	Glass defect crack	i) The front of lead terminals	A	$a \leq t, \quad b \leq 1/5W, \quad c \leq 3\text{mm}$	Max.3 defects allowed	
			B	Crack at two sides of lead terminals should not cover patterns and alignment mark		
		ii) Surrounding crack-non-contact side				
			$b < \text{Inner borderline of the seal}$			
		iii) Surrounding crack- contact side				
	$b < \text{Outer borderline of the seal}$					
iv) Corner			A	$a \leq t, \quad b \leq 3.0, \quad c \leq 3.0$		
	B	Glass crack should not cover patterns u and alignment mark and patterns.				

Inspection items	Judgment standard
	Category(application: B zone)

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16	PCB defect	<p>Component soldering:</p> <p>No cold soldering、short、open circuit、burr、tin ball</p> <p>The flat encapsulation component position deviation must be less than 1/3 width of the pin (Pic.1);</p> <p>the sheet component deviation:</p> <p>Pin deviates from the pad and contact with the near components is not permitted (Pic.2)</p>	 <p>Component</p> <p>Soldering pad Lead Component</p> <p>$L \leq W/2$ W</p> <p>$L1 > 0$ $L2 > 0$</p>
		<p>lead defect:</p> <p>The lead lack must be less than 1/3 of its width;</p> <p>The lead burr must be less than 1/3 of the seam;</p> <p>Impurities connect with the near leads is not permitted</p>	
		<p>Connector soldering:</p> <p>Soldering tin is at contact position of the plug and socket is not permitted</p> <p>No foundation is scald</p> <p>Serious cave distortion on plug and socket contact pin is not permitted</p>	 <p>head Base Board</p> <p>Soldering tin is not permit in this area</p> <p>Soldering tin is not permit in this area</p> <p>socket Base Board</p>
		<p>Glue on root of the speaker receiver and motor lead:</p> <p>The insulative coat of the lead must join into the PCB; the protected glue must envelop to the insulative coat.</p>	 <p>Glue Lead</p> <p>PCB Insulative coat</p>

10. Precautions for Use of LCD Modules

10.1 Handling Precautions

10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer.

Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents

10.1.6 Do not attempt to disassemble the LCD Module.

10.1.7 If the logic circuit power is off, do not apply the input signals.

10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- a. Be sure to ground the body when handling the LCD Modules.
- b. Tools required for assembly, such as soldering irons, must be properly ground.
- c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

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- d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range.

If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : $0^{\circ}\text{C} \sim 40^{\circ}\text{C}$

Relatively humidity: $\leq 80\%$

10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.